

F1G.

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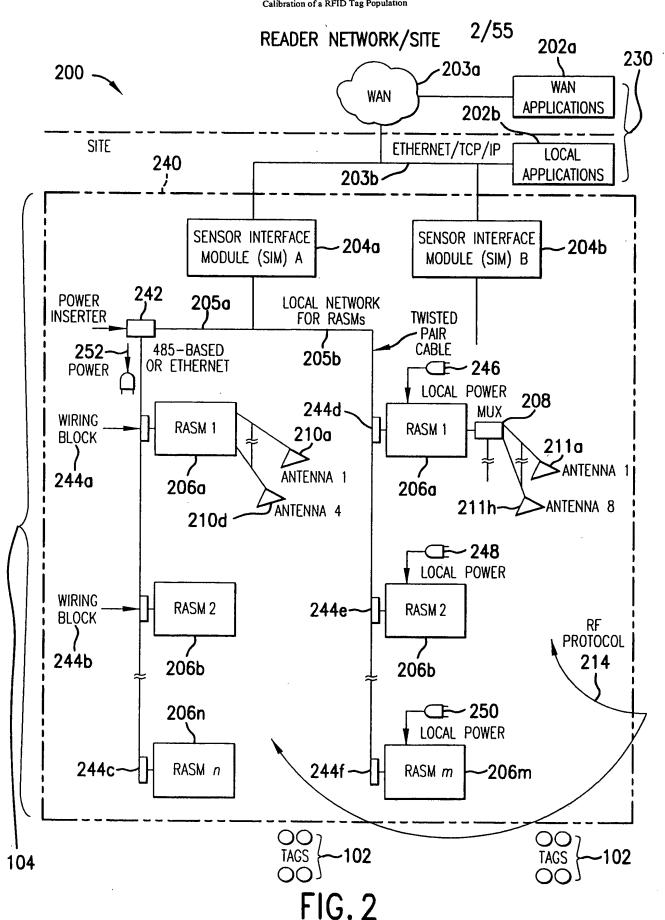
Appl. No. 10/072,870; Filed: Feb 12, 2002

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Inventors: Shanks, et al Tel: 202-371-2600

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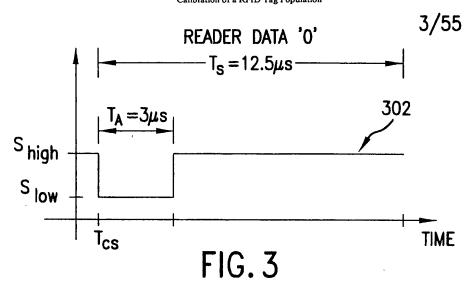
Appl. No. 10/072,870; Filed: Feb 12, 2002

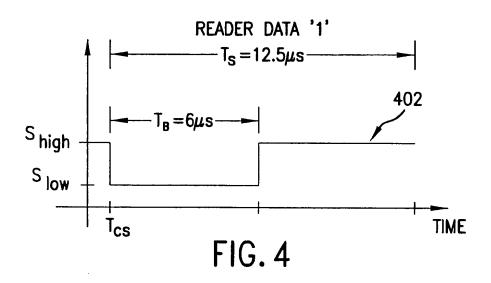
Dkt No. 1689.0270000; Group Unit: 2819

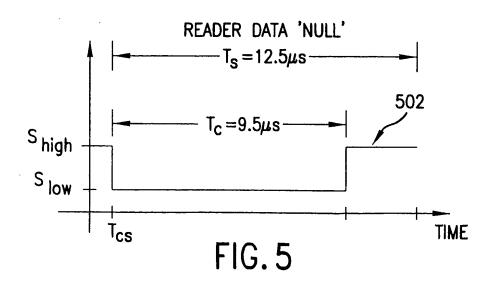
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READER BIT 'O' WITH TAG BIT 'O'

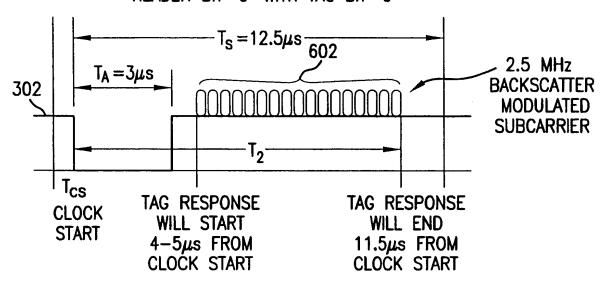


FIG. 6

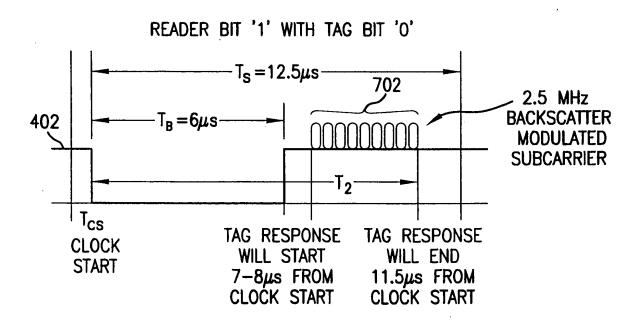


FIG. 7

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READER BIT '0' WITH TAG BIT '1'

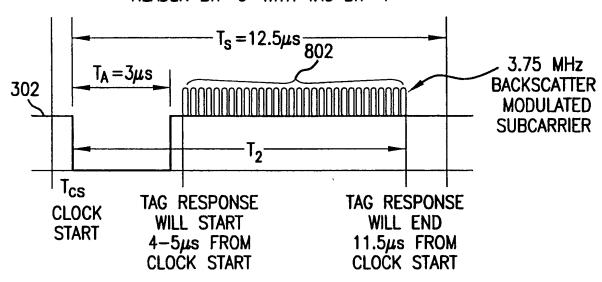


FIG.8

READER BIT '1' WITH TAG BIT '1'

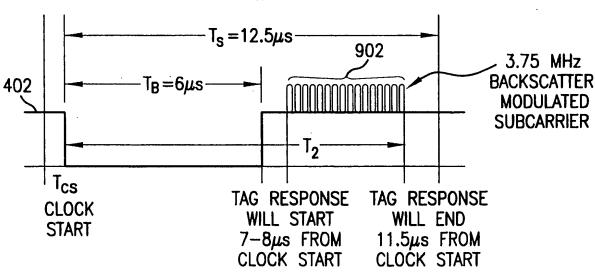


FIG. 9

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6/55 1001 -1002 1023 1020 DATA PROGRAMMING UNIT 1022 1024 1060b 1076 STATE MACHINE 1054 SAR 1056b 1074 1072 PWR 1028 -1060a BUS 1021 -1056a 1070 **COUNTER** 1030a 1030b 1026 1036 1066 RECEIVER RECEIVER **DMDER** 1053 <u>10</u>52a 1052b **OSCILLATOR DMDER CHARGE CHARGE** 1042 **PUMP PUMP** 1038 ~1032a ~1032b 1064 1034b 1 **MODULATOR MODULATOR** 1034a 1050a-1061b 1061a-1050b 1004a 1004d 1004b RF1 PAD RF2 PAD EXT PWR **GND** 1010a 1010b 1004c 1006 1008

FIG. 10

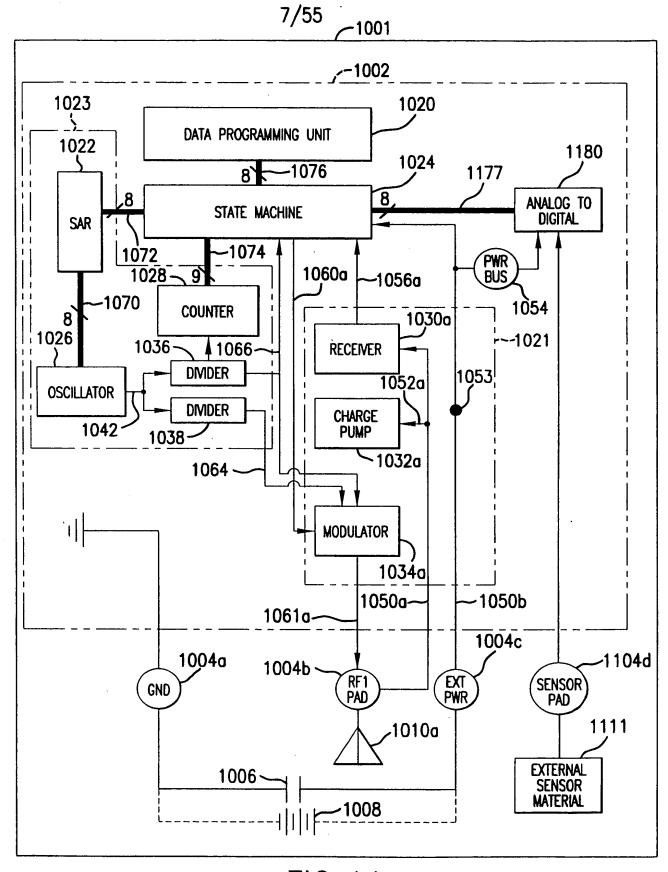


FIG. 11

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8/55 1202 INITIAL STATE DORMANT 1222 1220 INVALID (ANY) MASTER RESET SIGNALS/ **PULSE LENGTHS** 1204 CALIBRATION 1242 1224 VALID CALIBRATION PULSES 1206 DATA 'NULL' 1228 1226 COMMAND 1230 DATA '0 1232 DATA '1' DATA 0,1 1210 **MATCHING** -1208 1234 1236 : TREE-SUPERPOSITION 1244 TRAVERSAL DATA 0,1 DATA DATA NOT 'NULL' 'NULL' DATA 'NULL' DATA '0' & '1' MATCHING 1212 1238 DATA '0' & '1' 1240 MUTE 1202, 1204, 1206, 1202, 1204, 1206, 1208, 1210, 1212 1208, 1210, 1212 1204 1202 **MASTER MASTER** RESET DORMANT DORMAN **EVENT** 1220 1221

FIG. 12A

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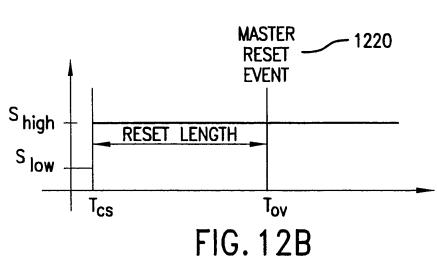
Dkt No. 1689.0270000; Group Unit: 2819

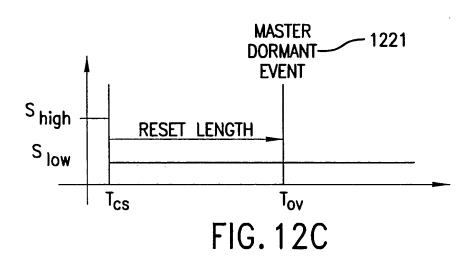
Inventors: Shanks, et al Tel: 202-371-2600

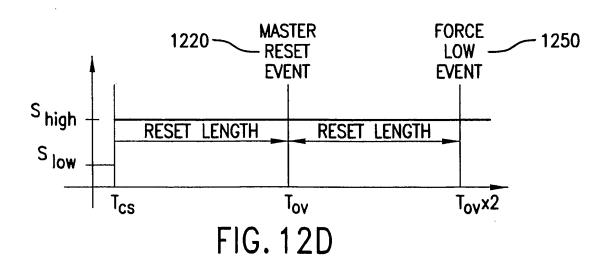
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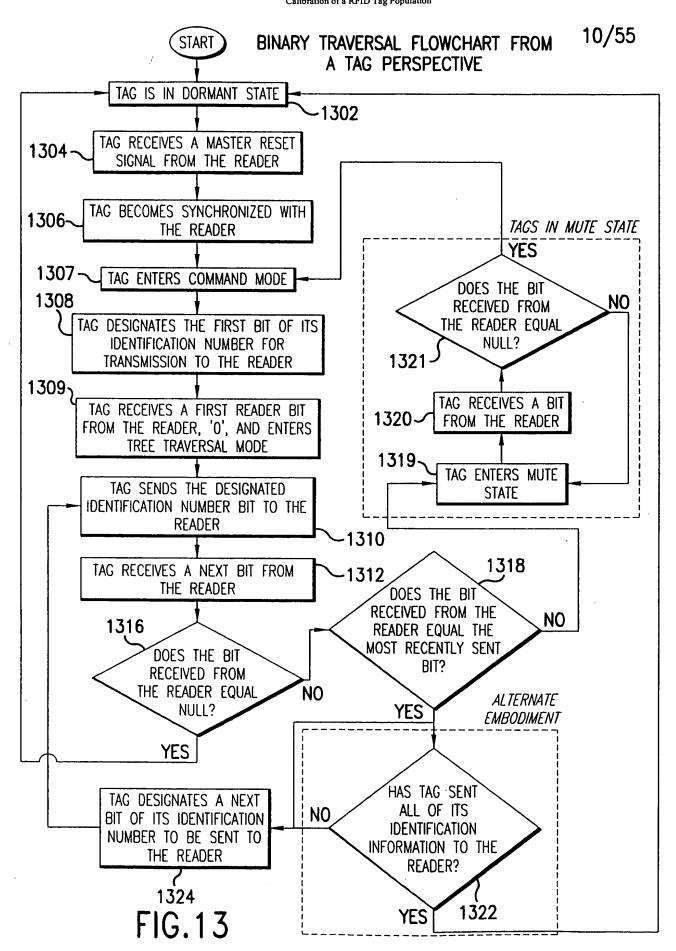
Appl. No. 10/072,870; Filed: Feb 12, 2002

Dkt No. 1689.0270000; Group Unit: 2819

Inventors: Shanks, et al Tel: 202-371-2600

Title: Method, System, and Apparatus for Remote Data

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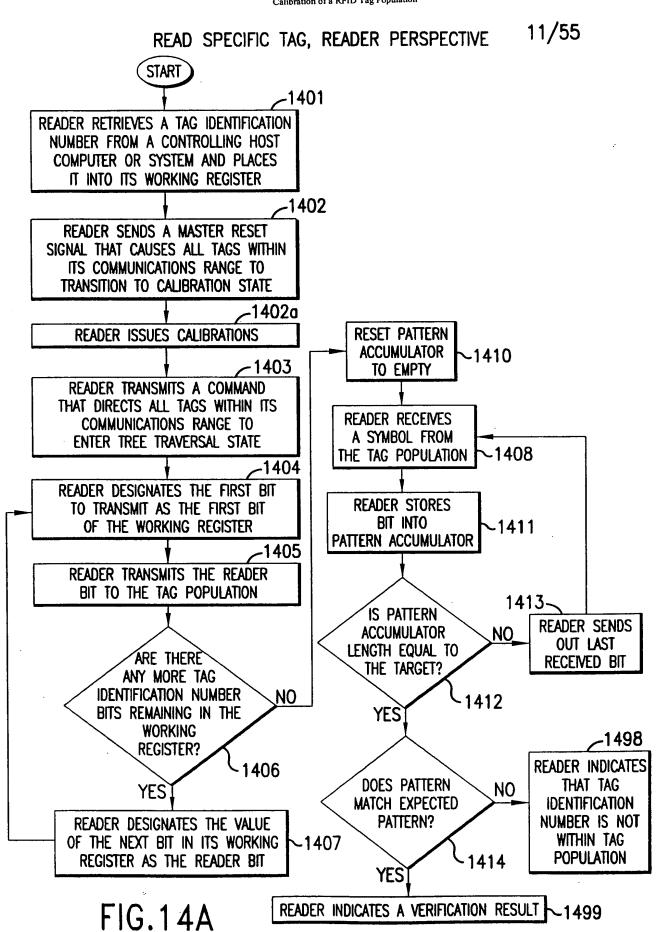


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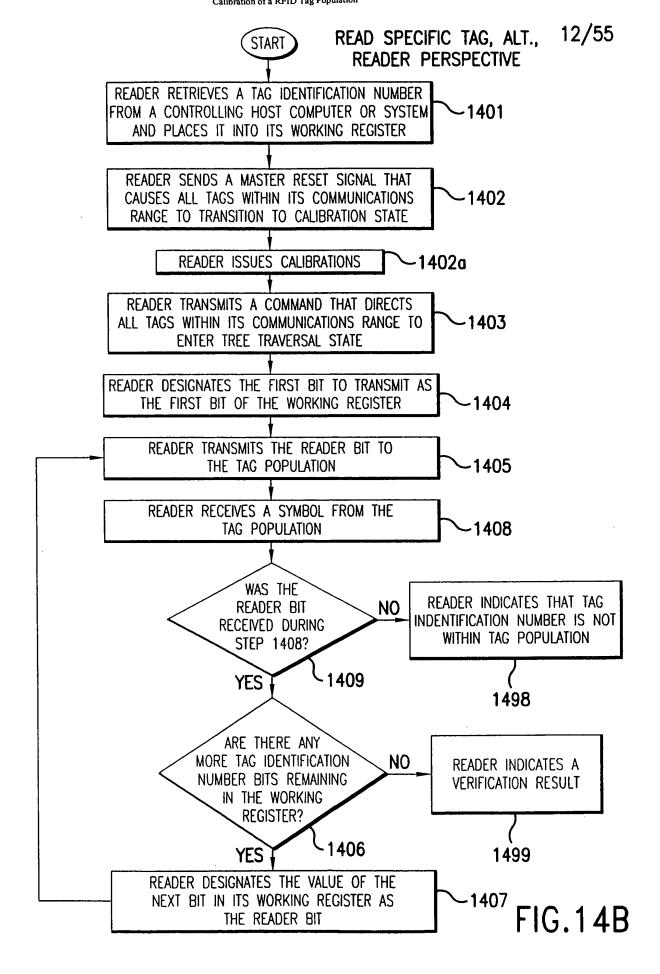
Appl. No. 16/072,870; Filed: Feb 12, 2002

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READ ALL TAGS, READER PERSPECTIVES

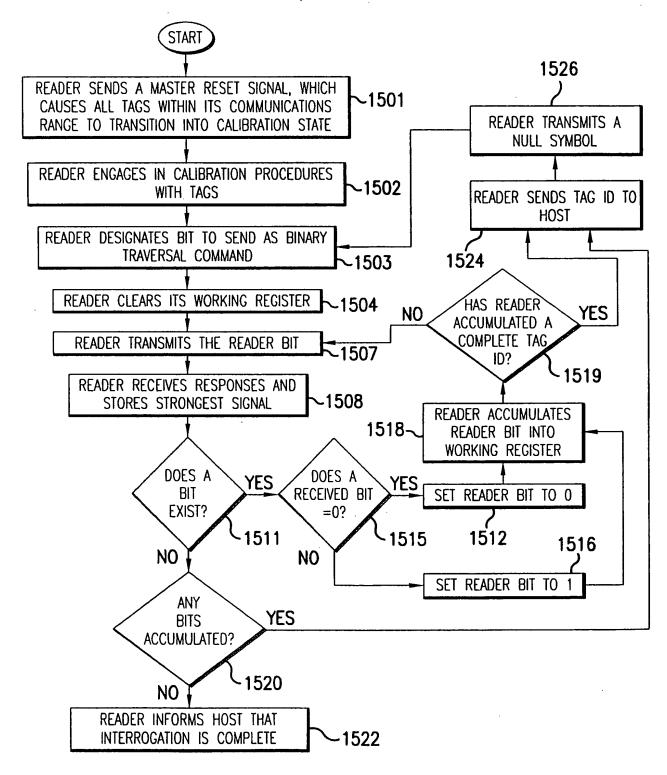
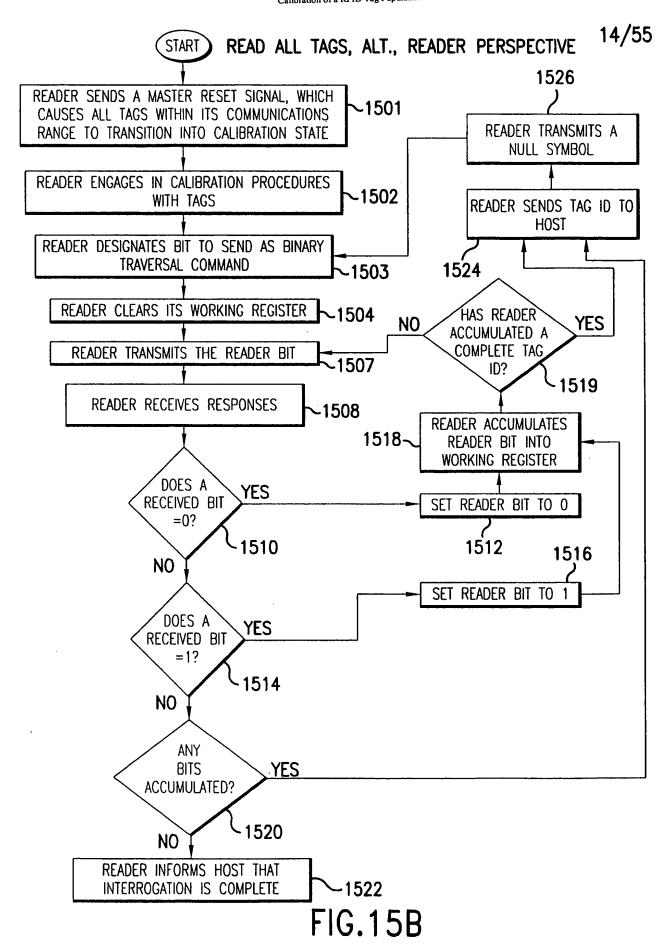


FIG.15A



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BINARY TRAVERAL PATHS AND TREE FOR A 3-BIT TAG POPULATION

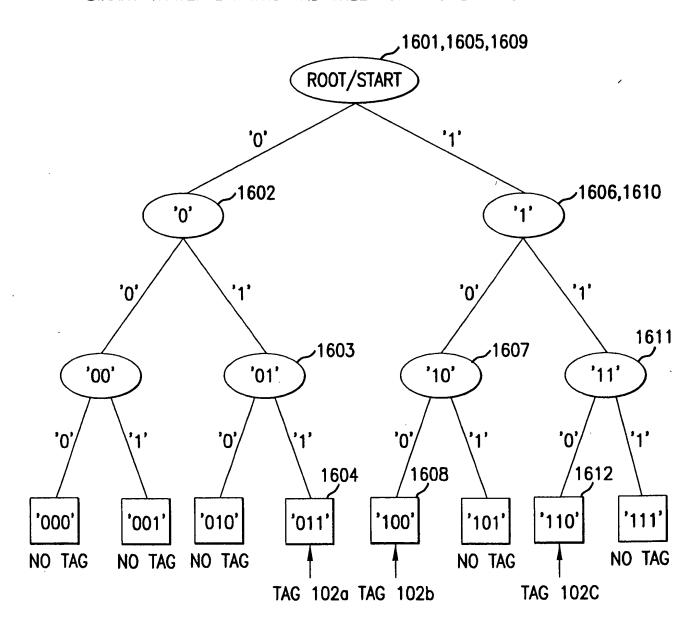
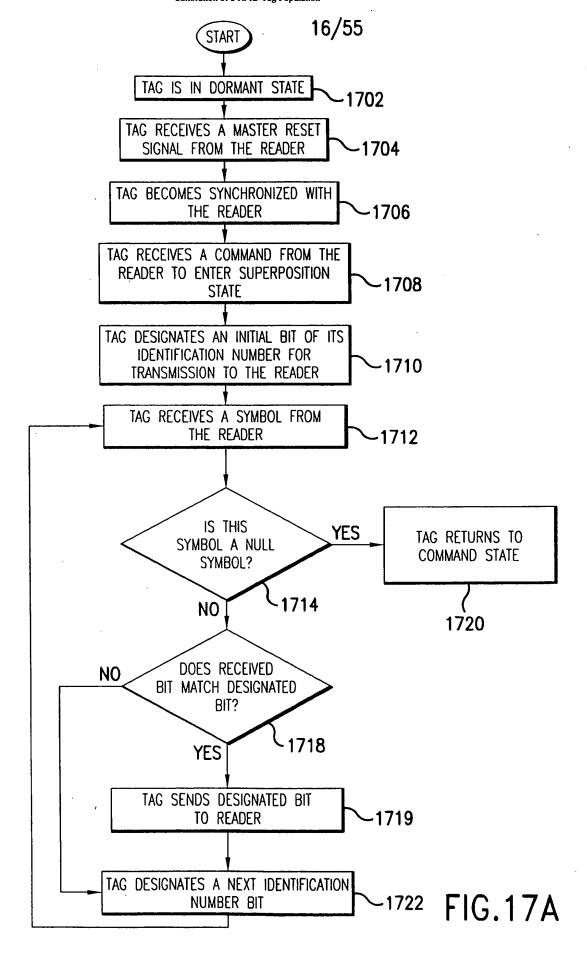


FIG. 16

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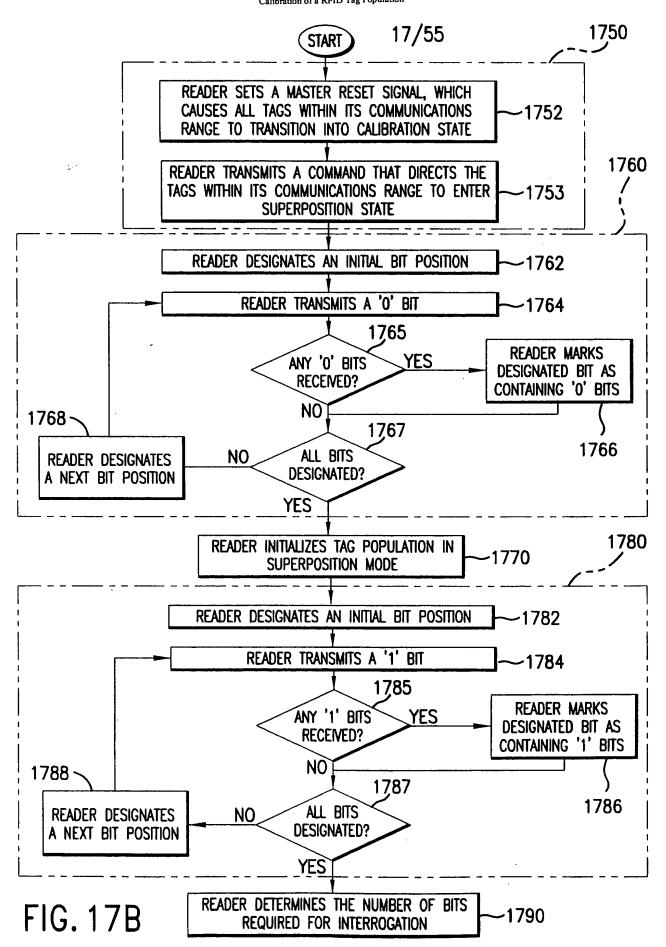
Appl. No. 10/072,870; Filed: Feb 12, 2002

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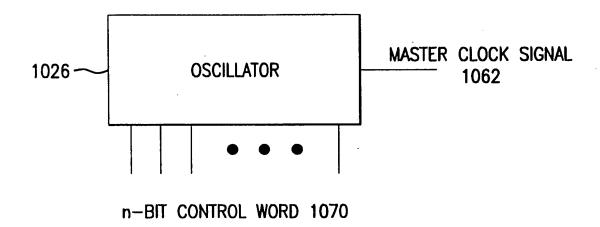


FIG. 18

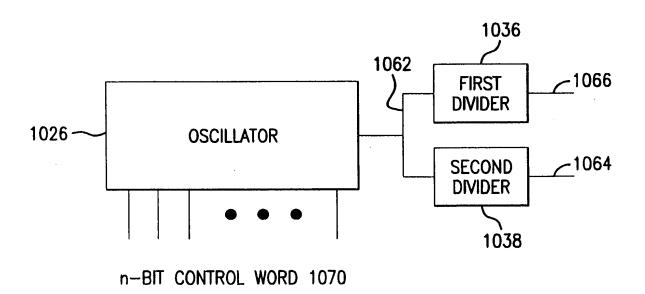


FIG. 19

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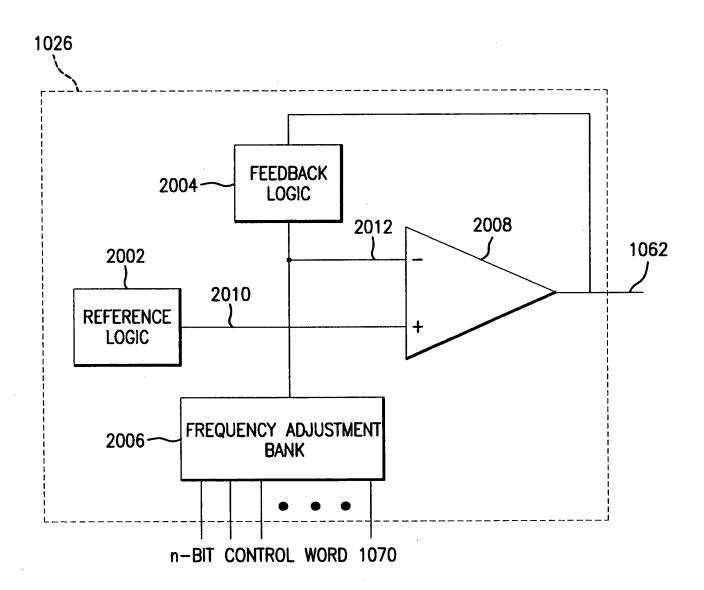


FIG. 20

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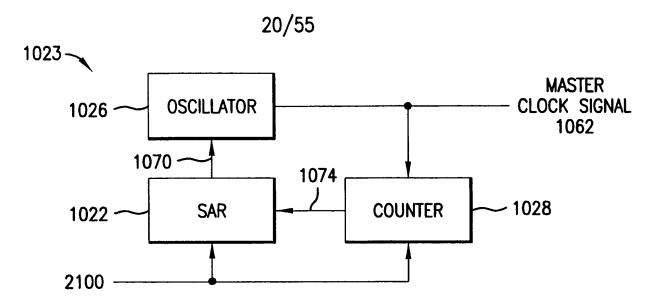
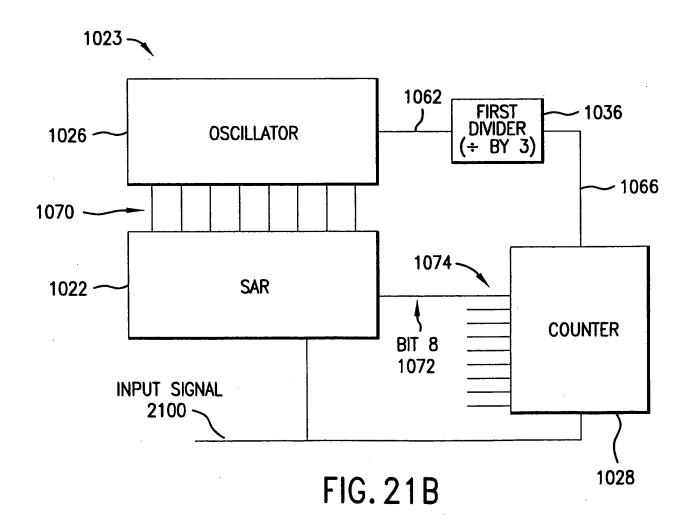
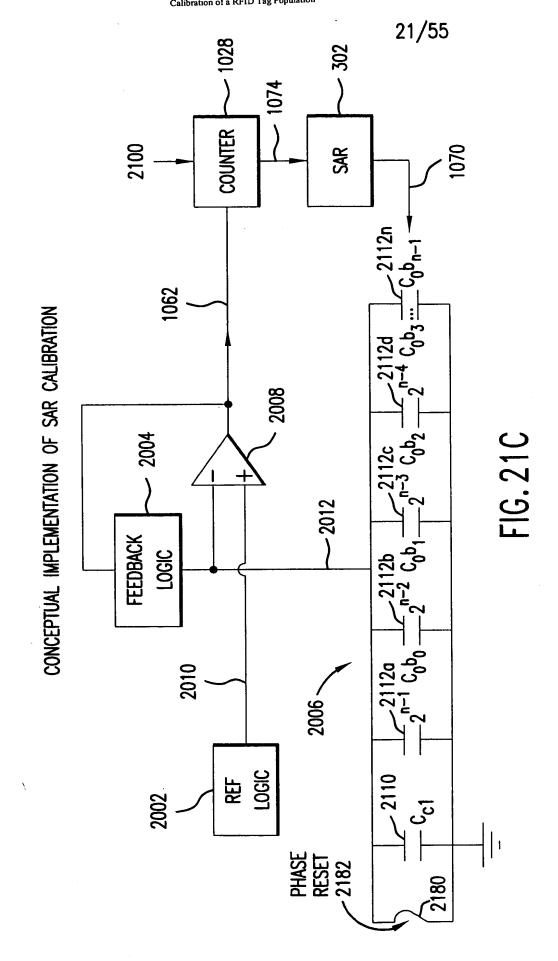


FIG. 21A



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PHYSICAL IMPLEMENTATION OF ADJUSTABLE CAPACITOR BANK

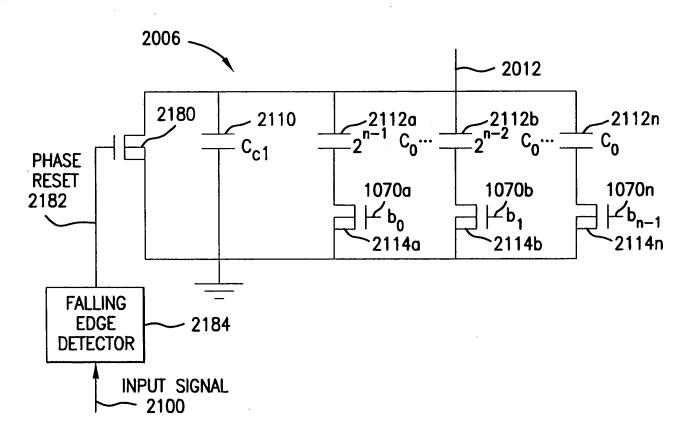
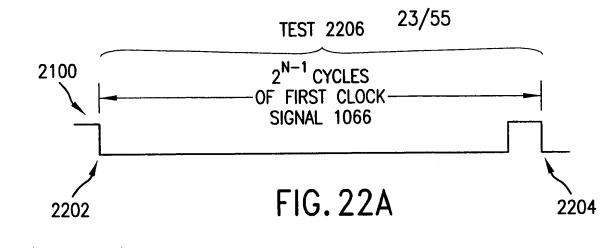


FIG. 21D

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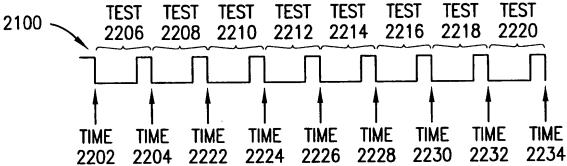
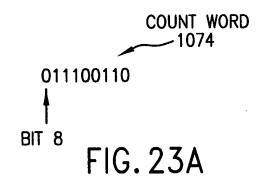


FIG. 22B



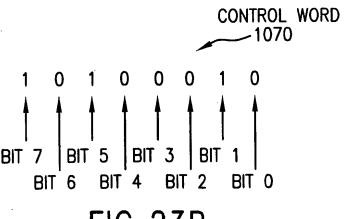
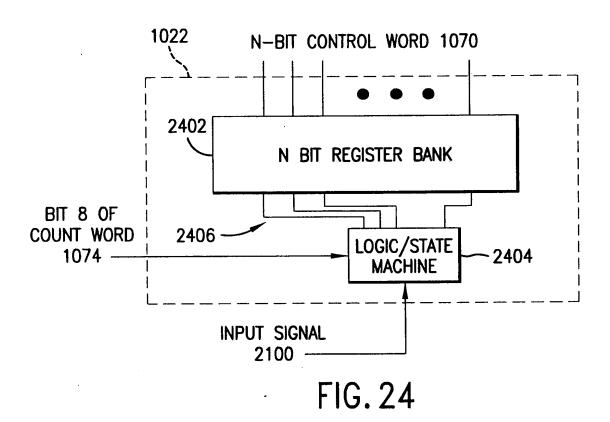


FIG. 23B

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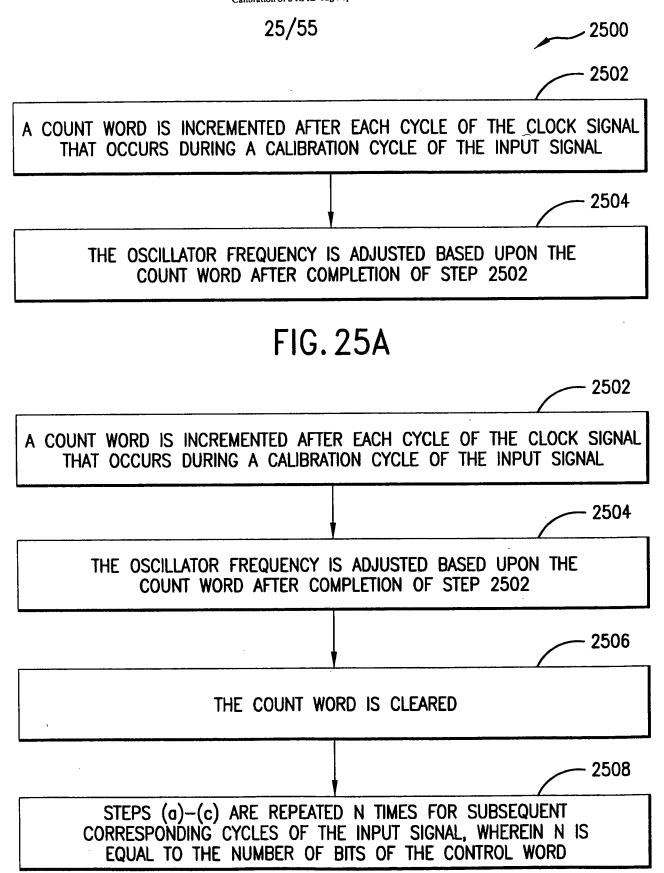


FIG. 25B

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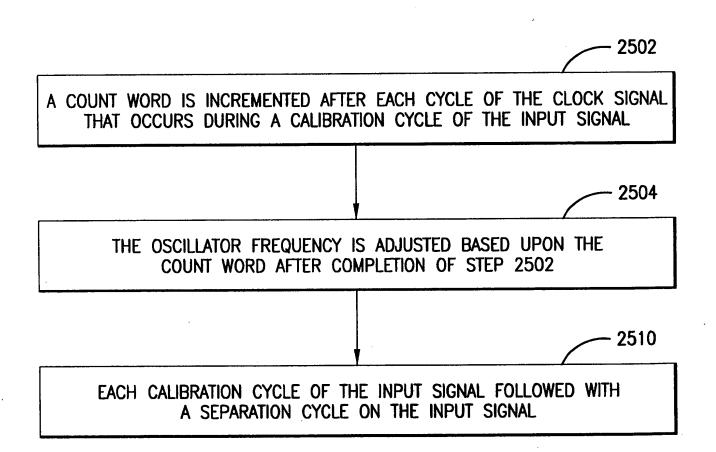


FIG. 25C

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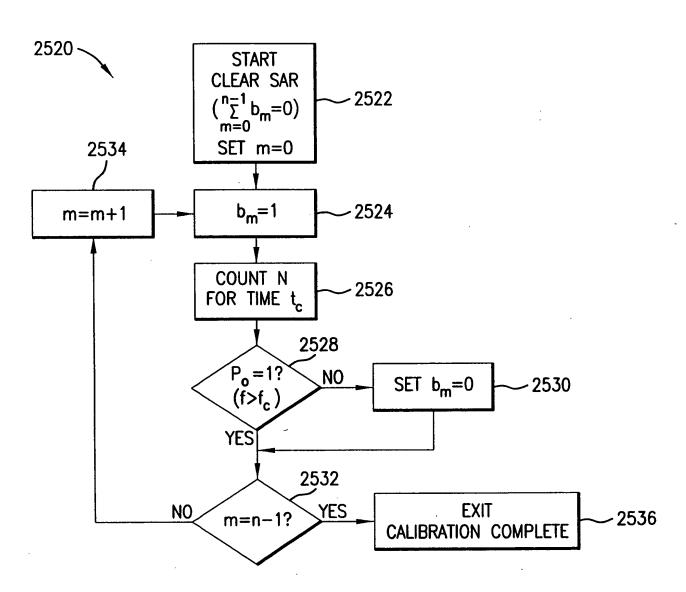


FIG. 25D

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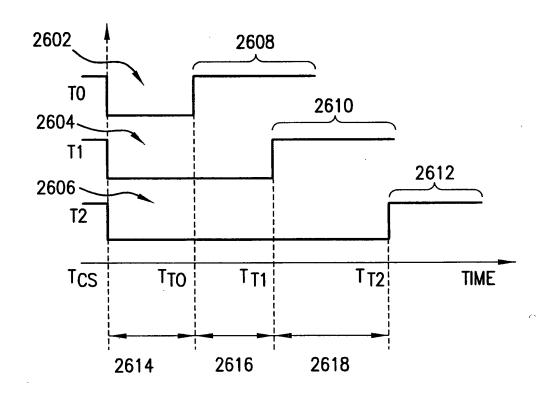
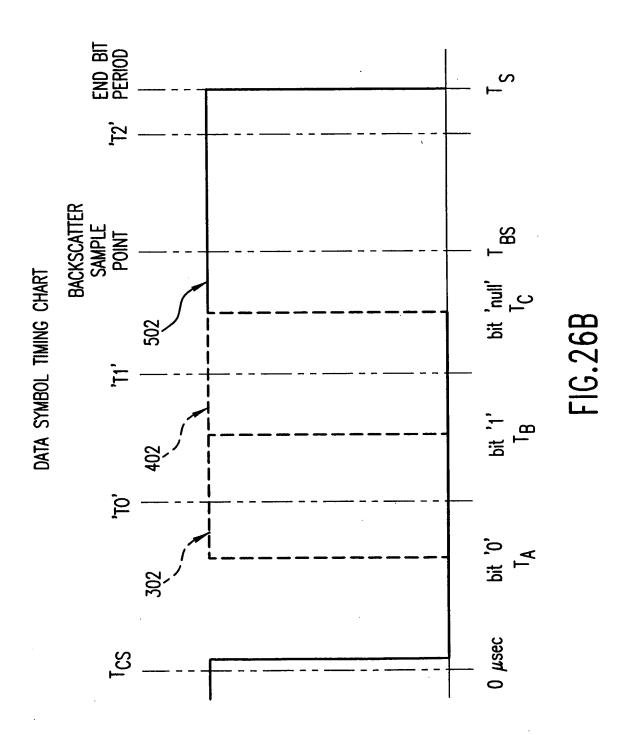


FIG.26A



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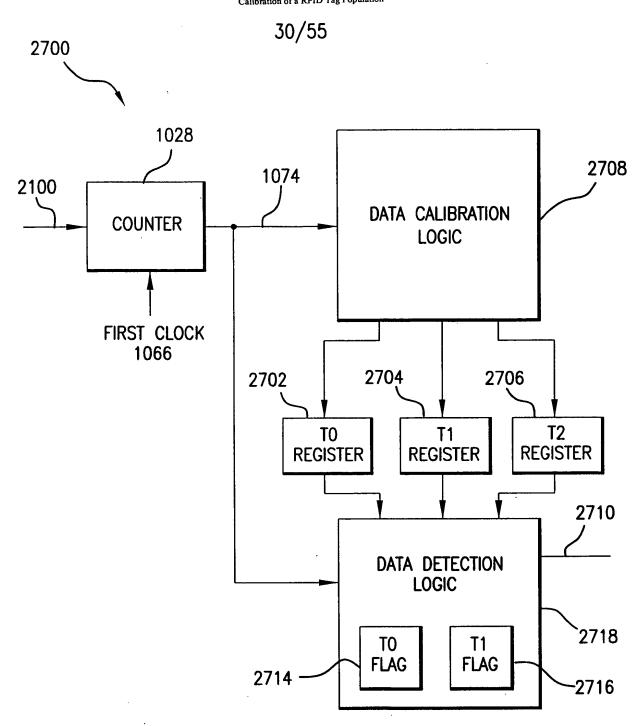


FIG.27

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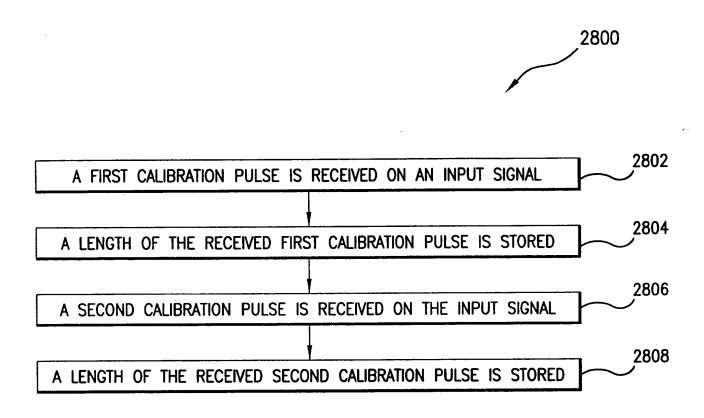


FIG.28A

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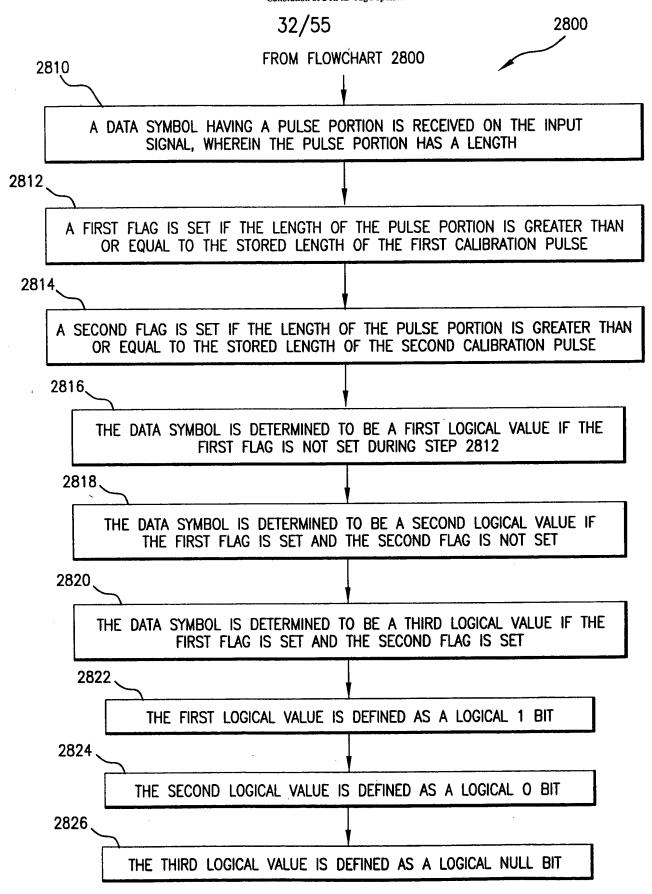


FIG.28B

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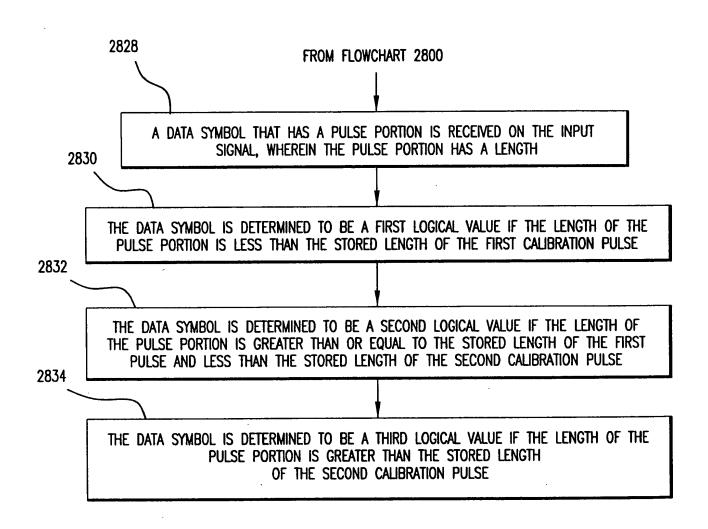


FIG.28C

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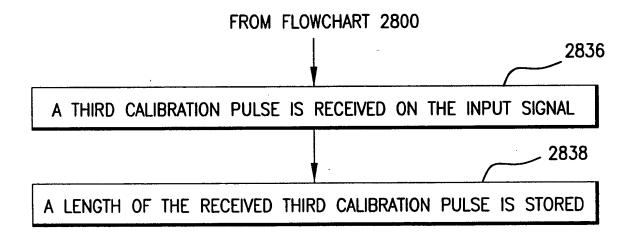
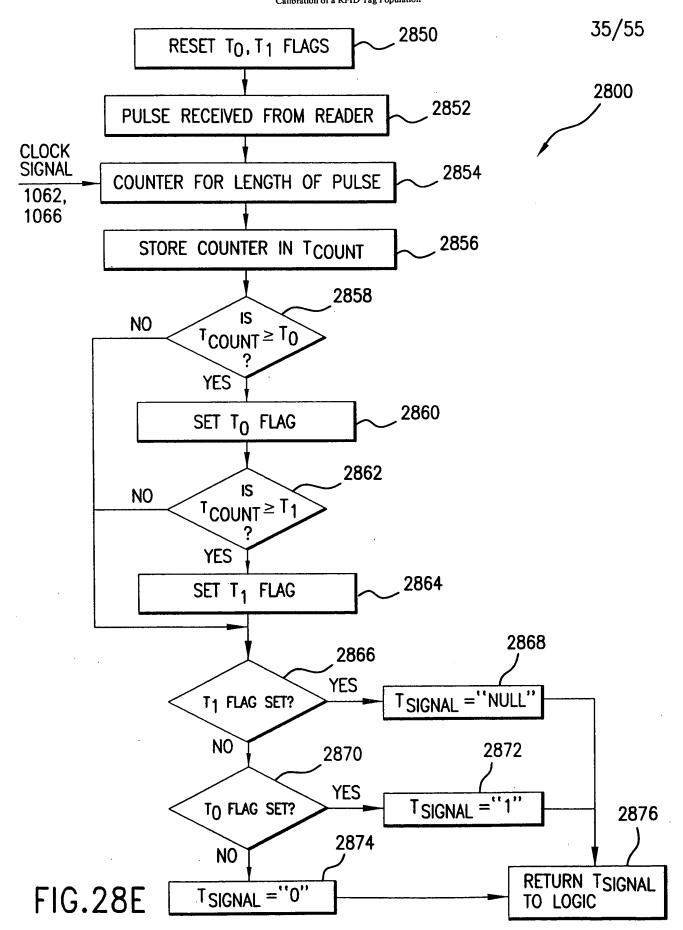
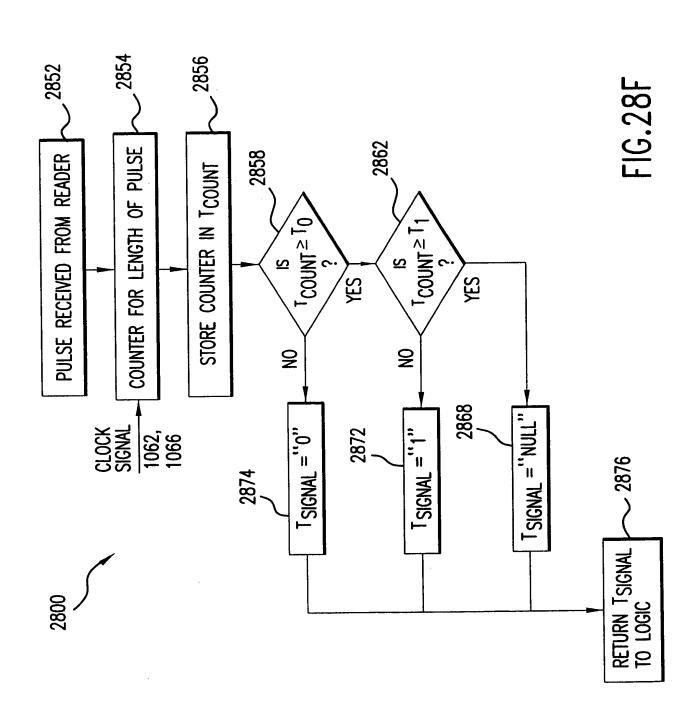


FIG.28D

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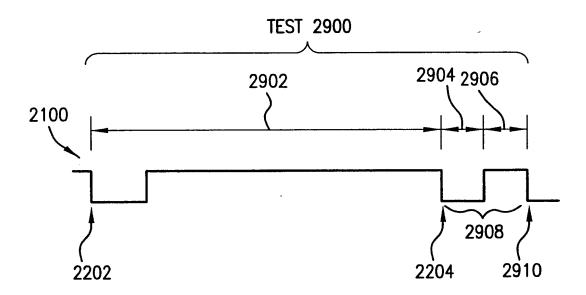


FIG.29

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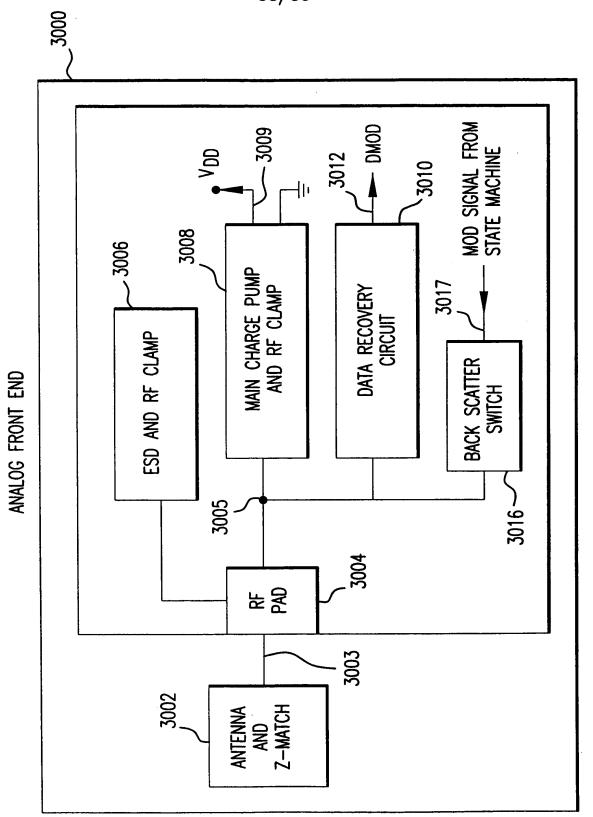


FIG.30

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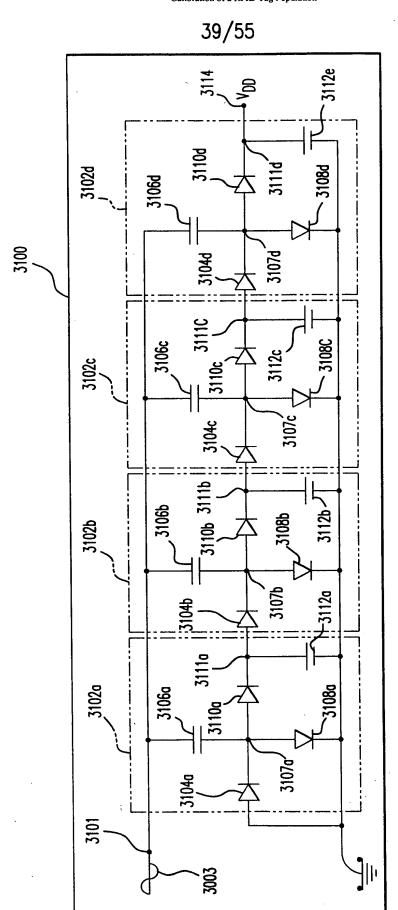
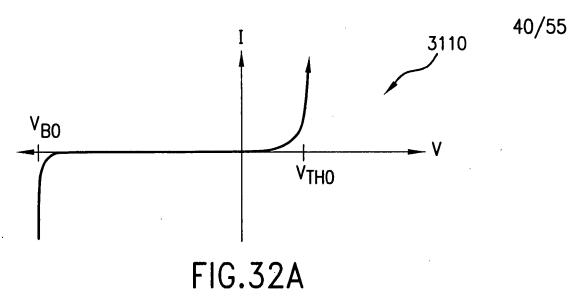
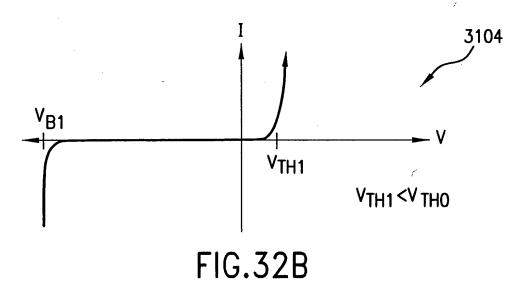


FIG.31





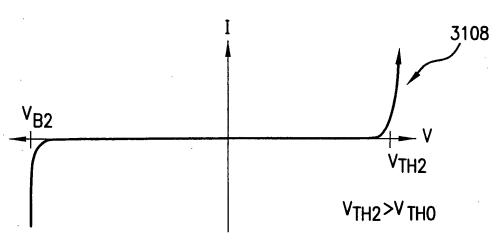


FIG.32C

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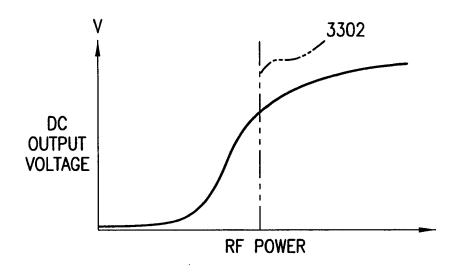


FIG.33A

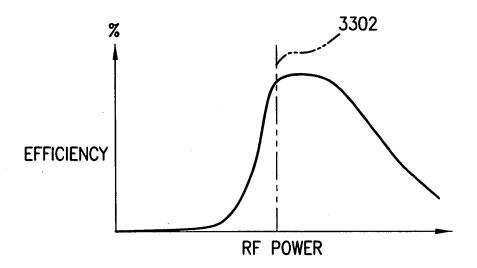


FIG.33B

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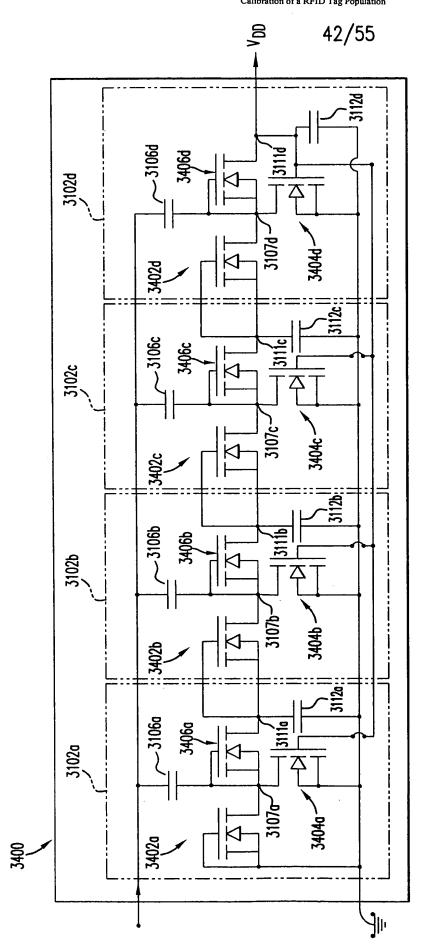


FIG. 34A

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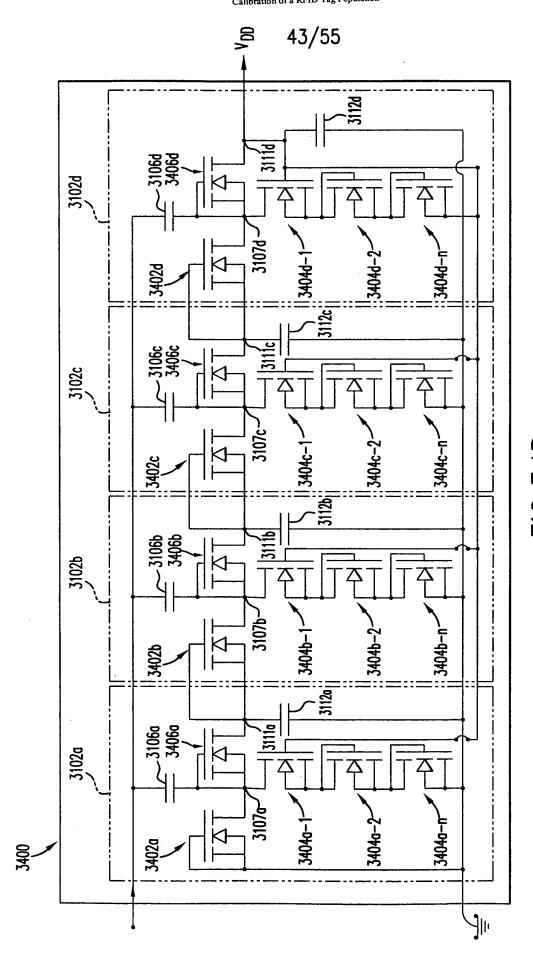


FIG.34B

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Inventors: Shanks, et al Tel: 202-371-2600

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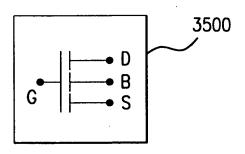


FIG.35A

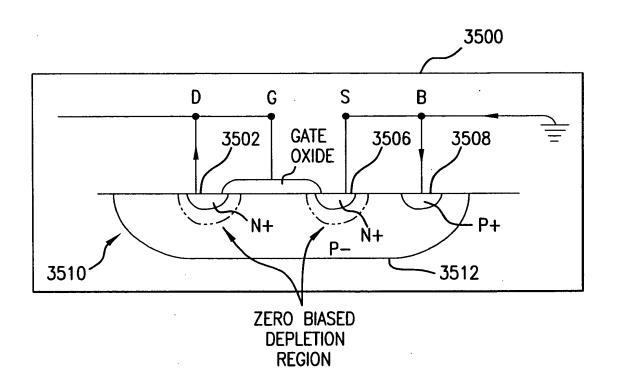


FIG.35B

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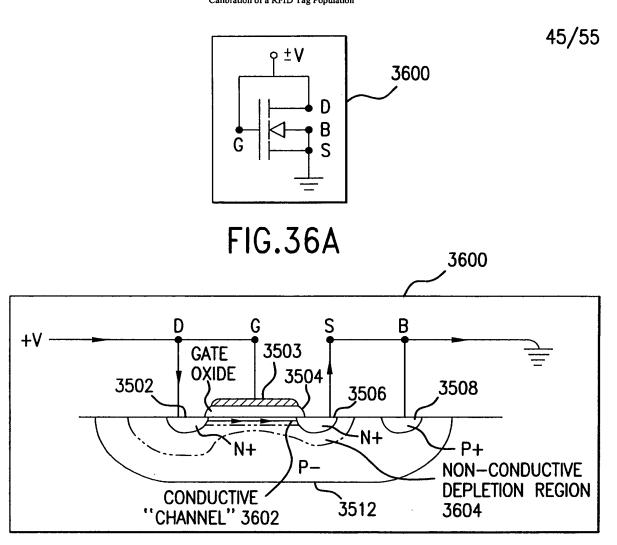


FIG.36B

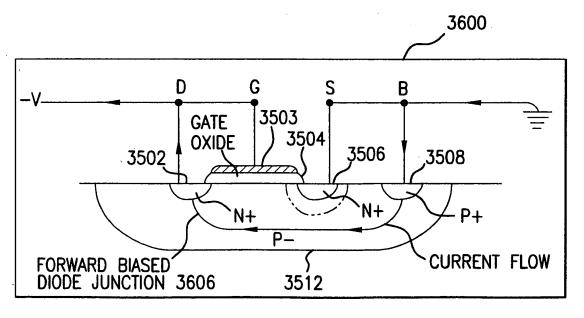


FIG.36C

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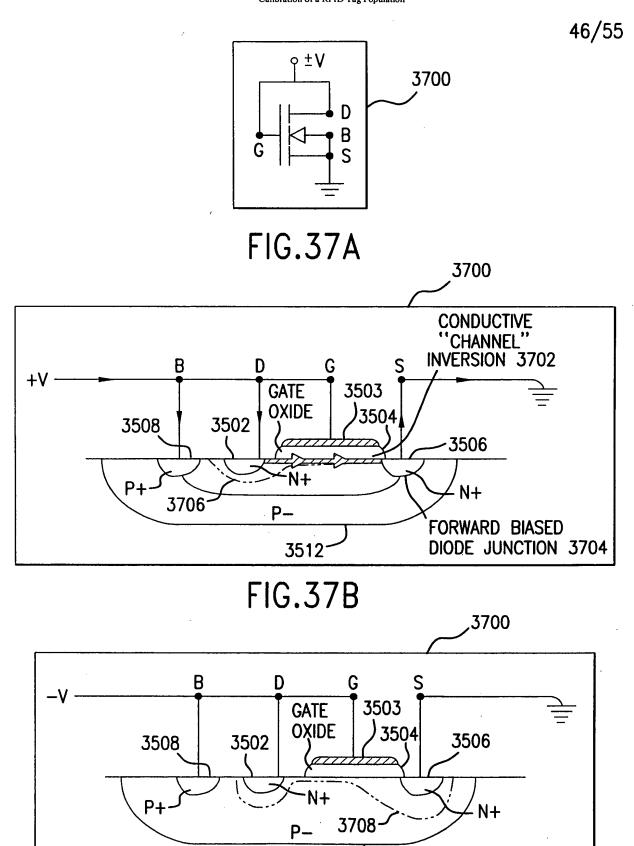
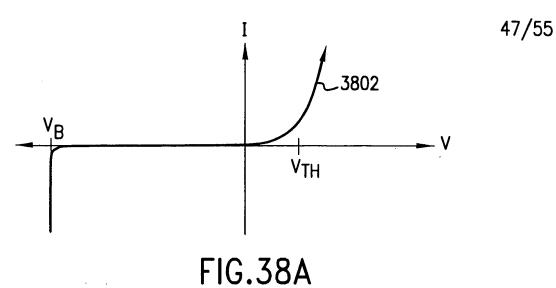


FIG.37C

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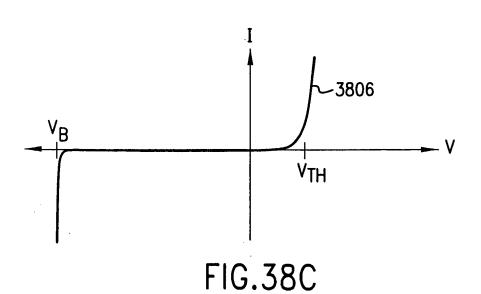
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 V_{PN} ÝTH

FIG.38B



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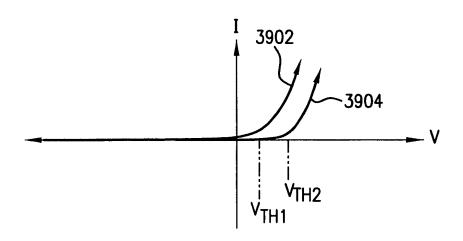


FIG.39

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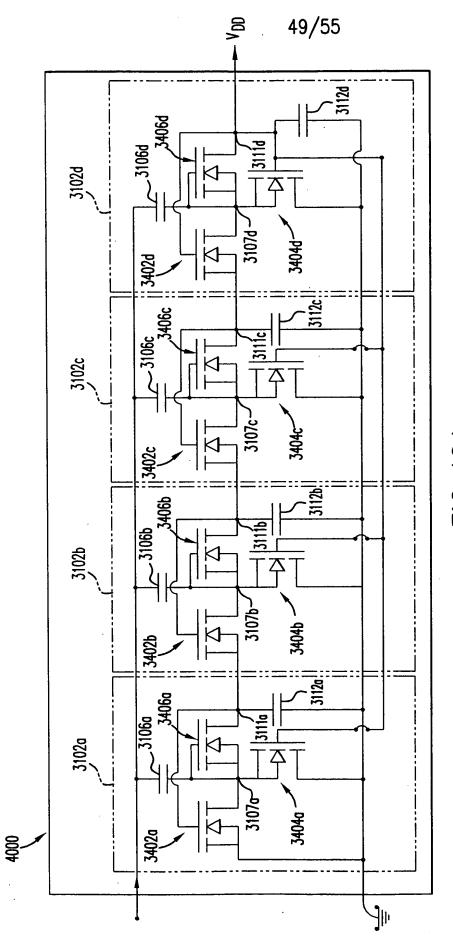


FIG.40A

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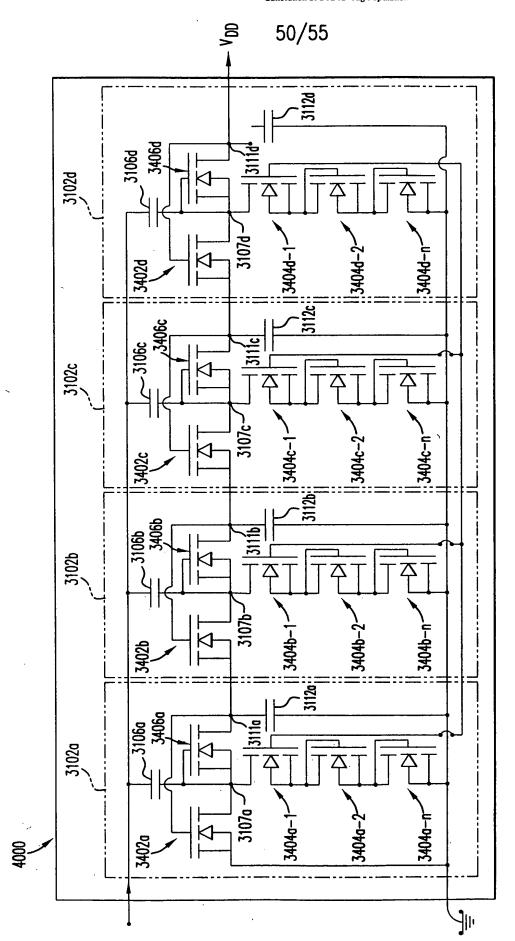
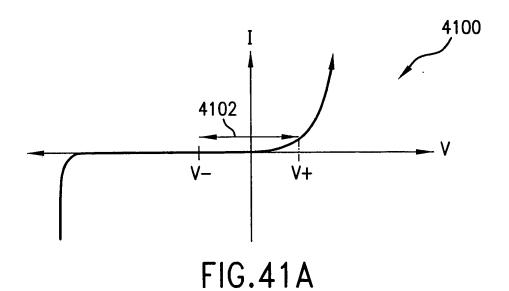
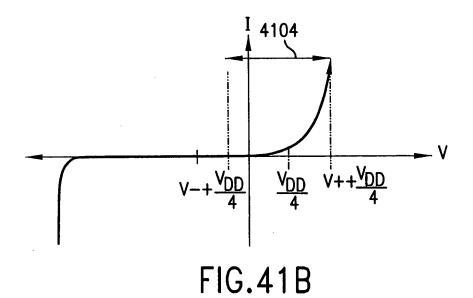


FIG.40B

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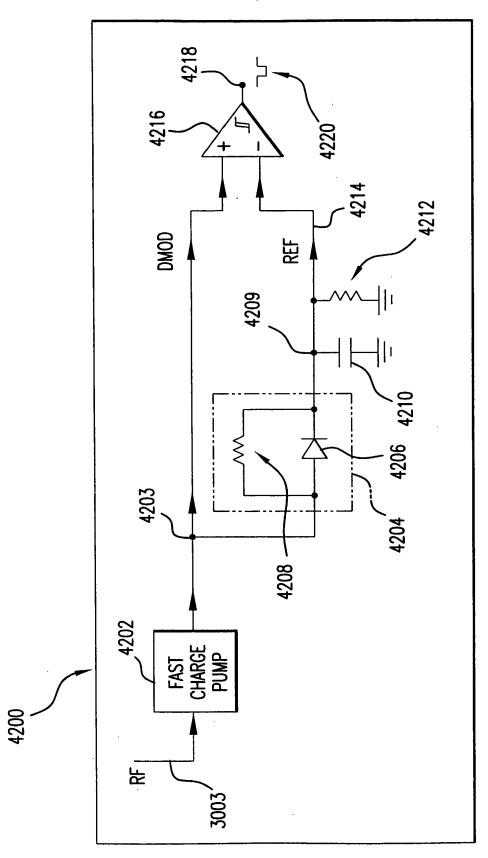


FIG. 42

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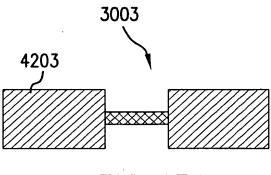


FIG.43A

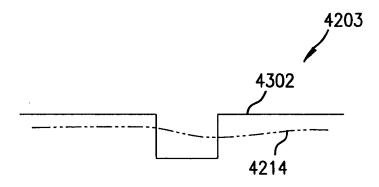


FIG.43B

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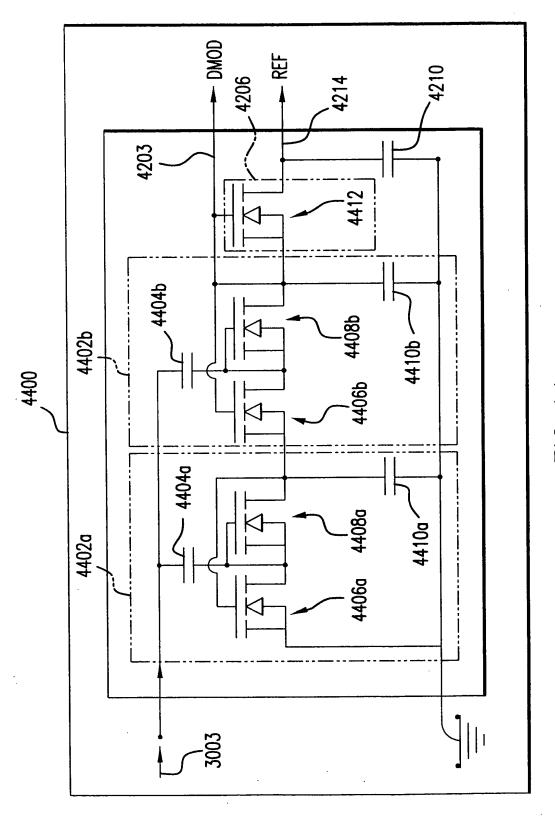


FIG.44

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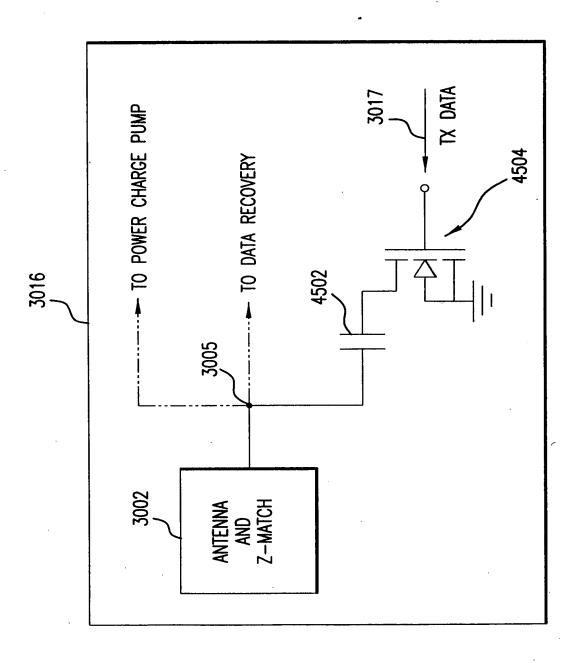


FIG.45